



Patent
Attorney's Docket No. 018414-133

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#12/c

In re Patent Application of)
William K. ZURAVLEFF et al.) Group Art Unit: 2305
Application No.: 08/480,738) Examiner: R. Phan
Filed: June 7, 1995)
For: NON-BLOCKING LOAD)
BUFFER AND A MULTIPLE-)
PRIORITY MEMORY SYSTEM)
FOR REAL-TIME MULTI-)
PROCESSING)

LOJ
11-6-97
entered
Cust. & Exam.
16 Jun 97
Filing
Cust. & Exam.

AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In complete response to the Official Action dated June 20, 1997, please enter the following amendments and remarks.

IN THE CLAIMS:

Cancel Claims 42, 48, 49, 59, 68, 69, 79 and 80.

Please amend the claims as follow:

1 39. (Once amended) A data processing system comprising:

one or more requesting processors that generate processor requests directed to one or more peripheral devices;

a plurality of peripheral devices that accept the processor requests;

and

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